

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,836,000 B1  
DATED : December 28, 2004  
INVENTOR(S) : Marr et al.

Page 1 of 3

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page,

Below *Assistant Examiner*, insert item:

-- [74] *Attorney, Agent, or Firm* - Schwegman, Lundberg, Woessner & Kluth, P.A. --.

Column 2,

Line 48, after "Fig.4B" delete ":".

Column 5,

Line 11, delete "SO" and insert -- SOI --, therefor.

Line 48, insert -- p-n -- before "junction".

Column 7,

Line 31, after "the" delete "CD".

Column 8,

Line 1, delete "In the in" and insert -- In the --, therefor.

Column 10,

Line 1, delete "to" before "difference".

Column 11,

Lines 16, 23 and 57, delete "a" and insert -- an --, therefor.

Lines 25, 58 and 59, delete "an" and insert -- a --, therefor.

Lines 37 and 39, after "first" delete "conductivity" and insert -- conductive --, therefor.

Column 12,

Lines 28 and 61, delete "a" and insert -- an --, therefor.

Lines 29, 30, 62 and 63, delete "an" and insert -- a --, therefor.

Column 13,

Line 13, delete "terminal and" and insert -- type in --, therefor.

Line 29, after "negative" delete "positive".

Line 30, delete "an" and insert -- a -- before "p-type".

Line 30, delete "a" and insert -- an --, therefor.

Line 42, insert -- first -- before "conductivity".

Line 60, after "comprises" insert -- coupling --, therefor.

Line 64, after "comprises" insert -- coupling --.

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Column 14,

Lines 2, 52 and 66, delete "then" and insert -- the --, therefor.

Lines 9, 13, 43, 47, 58 and 62, after "comprises" insert -- coupling --.

Lines 10, 11, 59 and 60, delete "an" and insert -- a --, therefor.

Line 11, delete "a p-type" and insert -- an n-type --, therefor.

Line 17, delete "then" and insert -- the --, therefor.

Lines 19-30, delete "A method of operating an integrated circuit comprising:

selecting an antifuse coupled between a first circuit and a second circuit in an integrated circuit;

coupling a first programming voltage to a well of a conductivity type in a substrate of a second conductivity type in the selected antifuse; and

coupling a second programming voltage to a conductive terminal of the second conductivity type in the selected antifuse to create a current path through an insulator between the conductive terminal and the well to program the selected antifuse." and insert -- A method of operating an integrated circuit comprising:

selecting an antifuse coupled between a circuit and an external pin in the integrated circuit; coupling a first programming voltage to the external pin that is coupled to a well of a first conductivity type in a substrate of a second conductivity type in the selected antifuse; and coupling a second programming voltage from the circuit to a conductive terminal of the second conductivity type in the selected antifuse to create a current path through an insulator between the conductive terminal and the well to program the selected antifuse. --, therefor.

Lines 41 and 56, delete "an" and insert -- the --, therefor.

Line 60, delete "a" and insert -- an --, therefor.

Column 15,

Lines 7, 22 and 33, delete "an" and insert -- the --, therefor.

Line 18, after "the" delete "selected".

Line 33, delete "an" and insert -- a -- before "p-type".

Line 33, delete "a" and insert -- an --, therefor.

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Column 16,

Lines 16, 17, 28 and 29, after "diffusion" insert -- region --, therefor.  
Lines 26, 28 and 29, delete "an" and insert -- a --, therefor.  
Line 26, delete "a" and insert -- an --, therefor.  
Line 33, after "forming" delete "a" and insert -- an --, therefor.

Signed and Sealed this

Twenty-fourth Day of May, 2005



JON W. DUDAS  
*Director of the United States Patent and Trademark Office*